

Syllabi & Scheme
for
M.Tech Course in
ELECTRONICS DESIGN
TECHNOLOGY
of
Calicut University

Scheme of M.Tech Programme in ELECTRONICS DESIGN TECHNOLOGY

(With Effect from the Academic Year 2011 onwards)

FIRST SEMESTER

Sl No	Course Code	Name of the Subject	Hours / Week			Internal	Sem End	Total	Sem-End Exam Duration	Credits
			L	T	P					
1.	EDT 11 101	Designing with Microcontrollers	3	1	0	100	100	200	3	4
2.	EDT 11 102	Advanced Digital System Design	3	1	0	100	100	200	3	4
3.	EDT 11 103	Electronic System Design	3	1	0	100	100	200	3	4
4.	EDT 11 104	Advanced Engineering Mathematics	3	1	0	100	100	200	3	4
5.	EDT 11 105	Elective 1	3	1	0	100	100	200	3	4
6.	EDT 11 106 (P)	Seminar	0	0	2	100	0	100	-	2
7.	EDT 11 107 (P)	Designing with Microcontrollers - Laboratory	0	0	2	100	0	100	-	2
		Total	15	5	4	700	500	1200		24
		Elective 1					-	-		
1.	EDT 11 105A	Embedded Programming								
2.	EDT 11 105B	Digital Integrated Circuit Design								
3.	EDT 11 105C	Data Communications and Networking								

L – Lecture, T- Tutorial, P – Practical

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SECOND SEMESTER

Sl No	Course Code	Name of the Subject	Hours / Week			Internal	End-Sem	Total	End-Sem Exam Duration	Credits
			L	T	P					
1.	EDT 11 201	Design of Digital Signal Processing Systems	3	1	0	100	100	200	3	4
2.	EDT 11 202	Multimedia Compression Techniques	3	1	0	100	100	200	3	4
3.	EDT 11 203	Product Design & Development	3	1	0	100	100	200	3	4
4.	EDT 11 204	Elective - II	3	1	0	100	100	200	3	4
5.	EDT 11 205	Elective - III	3	1	0	100	100	200	3	4
6.	EDT 11 206 (P)	Seminar	0	0	2	100	0	100	-	2
7.	EDT 11 207 (P)	Design of Digital Signal Processing Systems – Laboratory	0	0	2	100	0	100	-	2
		Total	15	5	4	700	500	1200		24
		Elective II					-	-		
1.	EDT 11 204A	Embedded Applications in Power Conversion								
2.	EDT 11 204B	Modern Control Systems Design								
3.	EDT 11 204C	Information Security								
		Elective III								
1.	EDT 11 205A	High Speed Digital Design								
2.	EDT 11 205B	ASIC & SOC								
3.	EDT 11 205C	Embedded OS & RTOS								

L – Lecture, T- Tutorial, P – Practical

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THIRD SEMESTER

Sl No	Course Code	Name of the Subject	Hours / Week			Marks		Total	End-Sem Exam Duration	Credits	
			L	T	P	Internal	Sem End				
1.	EDT 11 301	Elective IV	3	1	0	100	100	200	3	4	
2.	EDT 11 302	Elective V	3	1	0	100	100	200	3	4	
3.	EDT 11 303 (P)	Industrial Training	0	0	0	50	-	50	-	1	
4.	EDT 11 304 (P)	Master Research Project Phase I	0	0	22	Guide	EC*	-	300	-	6
						150	150				
TOTAL			6	2	22	550	200	750		15	
Elective IV							-	-			
1.	EDT 11 301A	Mixed Signal System Design									
2.	EDT 11 301B	Electronic Instrumentation Design									
3.	EDT 11 301C	Automotive Electronics									
Elective V											
1.	EDT 11 302A	Wireless Communication Systems									
2.	EDT 11 302B	Design of Switch-Mode Power Converters									
3.	EDT 11 302C	VLSI Signal Processing									

*EC – Evaluation committee, L – Lecture, T- Tutorial, P – Practical

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FOURTH SEMESTER

Sl No	Course Code	Name of the Subject	Hours / Week			Internal Evaluation		ESE*		Total	Credits
			L	T	P	Guide	Evaluation Committee	External Guide	Viva Voce		
1.	EDT 11 401(P)	Master Research Project Phase II	0	0	30	150	150	150	150	600	12
Total			-	-	30	150	150	150	150	600	12
			* The student has to undertake the departmental work assigned by HOD								
										Marks	Credits
								Grand Total		3750	75